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Patents

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: J.P. Baukus; L.W. Chow; &	)	Art Unit:
	)	
Serial No.: 10/619,981	)	Examiner:
	)	
Filed: 7/14/2003	)	RE: STATUS REQUEST
	)	Our Ref.: 621039-4
FOR: Programmable Connector/Isolator	)	B-3947DIV
and Double Polysilic Layer CMOS	)	
Process with Buried Contact Using	)	DATE: 7/14/2004
the Same	)	

Honorable Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

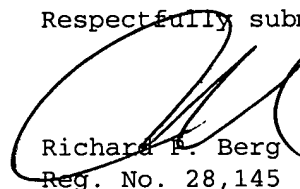
Sir:

It will be appreciated if you will inform the undersigned as to the status of the above-identified U.S. patent application.

This inquiry is being initiated on the basis of the fact that the above-identified application was filed on 7/14/2003 and an official action dealing substantively with this application has not been received.

Please inform the undersigned as to when we may expect to receive an official action for the above-identified application.

Respectfully submitted,



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Reg. No. 28,145

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